

# BN-GLC-BX-D & BN-GLC-BX-U

1.25Gbps 1310/1490nm(1490/1310nm) 20km LC BIDI SFP Transceiver



BN-GLC-BX-D



BN-GLC-BX-U

## Features

- ◆ Up to 1.25Gbps data rate
- ◆ Bi-Directional LC receptacle optical interface compliant
- ◆ Single +3.3V power supply
- ◆ DDMI function available with internally calibrated mode
- ◆ Hot-pluggable
- ◆ Receiver loss of signal output
- ◆ Transmitter disable input
- ◆ International Class1 laser safety certified
- ◆ Operating temperature range:  
Commercial: -5°C~70°C Industrial:  
-40°C~85°C
- ◆ CE,FCC,RoHS Compliant

## Application

- ◆ Fast Ethernet
- ◆ Router/Server Interface
- ◆ Switch to Switch Interface
- ◆ Switched Backplane Applications
- ◆ Other Optical Links

## Standard

- ◆ Compliant with SFP MSA (INF-8074i)
- ◆ Compliant with SFF-8472
- ◆ Compliant with IEEE 802.3z

## Specification

Absolute Maximum Ratings				
Parameter	Symbol	Min	Max	Unit
Storage temperature	TS	-40	85	°C
Power Supply Voltage	V <sub>CC</sub>	0	3.6	V
Relative Humidity	RH	5	95	%

Recommended Operating Conditions						
Parameter	Symbol	Min	Typical	Max	Unit	
Operating Case Temperature	T <sub>C</sub>	-5		70	°C	
		-40		85		
Power Supply Voltage	V <sub>CC</sub>	3.13	3.3	3.47	V	
Data Rate			1.25		Gbps	
Fiber Length 9/125μm core SMF				20	km	

Electrical Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Total Supply Current	I <sub>CC</sub>			300	mA	
Transmitter						
Transmitter Differential Input Voltage		400		2400	mV	
Tx_Fault Output Voltage - High	V <sub>OH</sub>	2.4		V <sub>CC</sub>	V	LVTTL
Tx_Fault Output Voltage - Low	V <sub>OL</sub>	0		0.4	V	LVTTL
Tx_Disable Input Voltage - High	V <sub>IH</sub>	2		V <sub>CC</sub>	V	LVTTL
Tx_Disable Input Voltage - Low	V <sub>IL</sub>	0		0.8	V	LVTTL

Electrical Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Input Differential Impedance	Z <sub>IN</sub>	85	100	115	Ω	
Receiver						
Receiver Differential Output Voltage		600		1600	mV	
LOS Output Voltage - High	V <sub>OH</sub>	2.4		V <sub>CC</sub>	V	LVTTL
LOS Output Voltage - Low	V <sub>OL</sub>	0		0.4	V	LVTTL
Output Differential Impedance	Z <sub>OUT</sub>	90	100	110	Ω	

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Optical Transmitter Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Average Output Power	P <sub>OUT</sub>	-9		-3	dBm	
Center Wavelength	λ <sub>c</sub>	1260	1310	1360	nm	
		1530	1550	1570		
Spectral Width	Δλ			3.5	nm	
				1		
Side Mode Suppression Ratio	SMSR	30			dB	
Extinction Ratio	ER	9			dB	
Transmitter OFF Power	P <sub>OFF</sub>			-45	dBm	
Jitter P-P	T <sub>J</sub>			0.1	UI	
Output Eye Diagram	Complies with IEEE802.3z					
Optical Receiver Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Center Wavelength	λ <sub>c</sub>	1530	1550	1570	nm	
		1260	1310	1360		
Receiver Sensitivity	P <sub>SEN</sub>			-20	dBm	
Input Saturation Power (Overload)	P <sub>SAT</sub>	-3			dBm	
LOS De-assert Level	LOSD			-21	dBm	
LOS Assert Level	LOSA	-39			dBm	
LOS hysteresis	HYS	0.5		6	dB	

Note 1. Measured with PRBS27-1 pattern, @1.25Gbps, ER=9dB, BER=1x10<sup>-12</sup>.

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## Pin Definition

20	VeeT
19	TD-
18	TD+
17	VeeT
16	VccT
15	VccR
14	VeeR
13	RD+
12	RD-
11	VeeR

Top of Board

1	VeeT
2	Tx_Fault
3	Tx_disable
4	MOD-DEF(2)
5	MOD-DEF(1)
6	MOD-DEF(0)
7	Rate Select
8	LOS
9	VeeR
10	VeeR

Bottom of Board

As Viewed Through Top of Board

# BN-GLC-BX-D & BN-GLC-BX-U

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Pin No	Symbol	Name/Description	Power Seq.	Notes
1	VeeT	Transmitter Ground	1st	
2	TX Fault	Transmitter Fault Indication, Logic 1 indicates Transmitter Fault.	3rd	1
3	TX Disable	Transmitter Disable, Transmitter disables on high or open.	3rd	2
4	MOD-DEF(2)	Module Definition 2. Data line for two wire Serial ID.	3rd	3
5	MOD-DEF(1)	Module Definition 1. Clock line for two wire Serial ID.	3rd	3
6	MOD-DEF(0)	Module Definition 0. Grounded within the module.	3rd	3
7	Rate Select	Not Connected	3rd	
8	LOS	Loss of Signal indication. Logic 1 indicates Loss of Signal.	3rd	4
9	VeeR	Receiver Ground	1st	
10	VeeR	Receiver Ground	1st	
11	VeeR	Receiver Ground	1st	
12	RD-	Inverse Received Data Out, AC coupled	3rd	
13	RD+	Received Data Out, AC coupled	3rd	
14	VeeR	Receiver Ground	1st	
15	VccR	Receiver Power	2nd	
16	VccT	Transmitter Power	2nd	

Pin No	Symbol	Name/Description	Power Seq.	Notes
17	VeeT	Transmitter Ground	1st	
18	TD+	Transmit Data In, AC coupled	3rd	
19	TD-	Inverse Transmit Data In, AC coupled	3rd	
20	VeeT	Transmitter Ground	1st	

**Note 1.** TX Fault is open collector/drain output which should be pulled up externally with a 4.7k - 10kΩ resistor on the host board. Pull up voltage between 2.0V and VccT/R+0.3V. When high, this output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to <0.8V.

**Note 2.** TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7k - 10kΩ resistor. It's states are:

Low (0 - 0.8V): Transmitter on  
(0.8 - 2.0V): Undefined  
High (2.0 - 3.465V): Transmitter Disabled  
Open: Transmitter Disabled

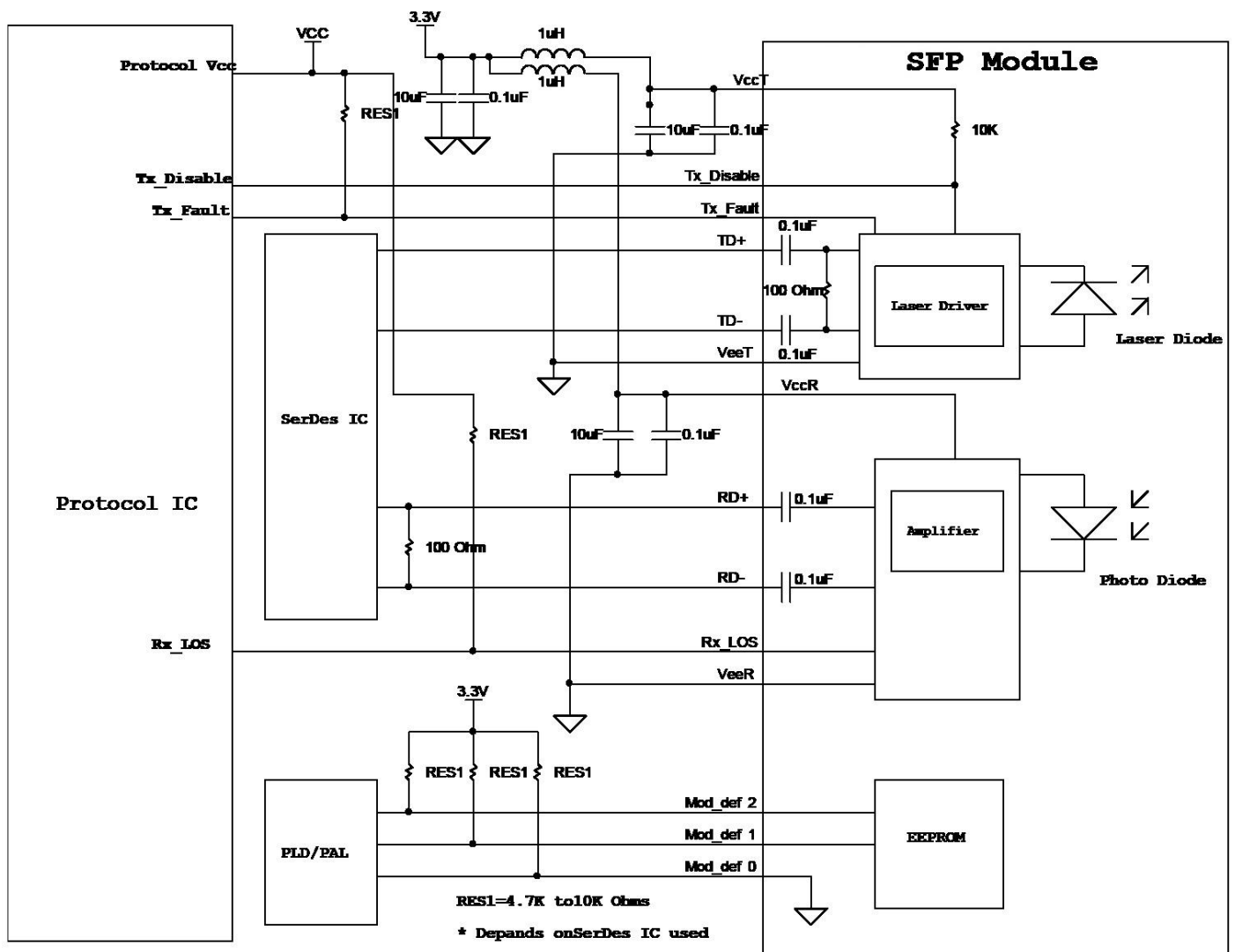
Note 3. Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7k - 10kΩ resistor on the host board. The pull-up voltage shall be VccT or VccR.

Mod-Def 0 is grounded by the module to indicate that the module is present  
Mod-Def 1 is the clock line of two wire serial interface for serial ID

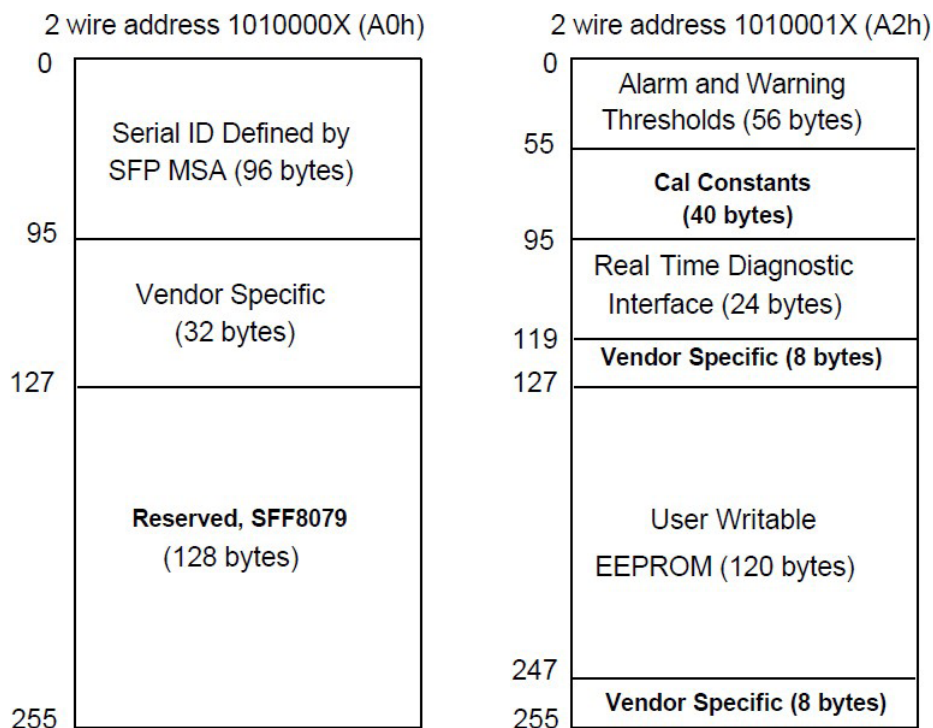
Mod-Def 2 is the data line of two wire serial interface for serial ID

Note 4. LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7k - 10kΩ resistor. Pull up voltage between 2.0V and VccT/R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

### Typical Application Circuit



## Digital Diagnostic Memory Map



## EEPROM Serial ID Memory Contents

The optical transceiver contains an EEPROM. It provides access to sophisticated identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information. When the serial protocol is activated, the host generates the serial clock signal (SCL, Mod Def 1). The positive edge clocks data into those segments of the EEPROM that are not writing protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. The diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 - 95 at wire serial bus address A2h. The digital diagnostic memory map specific data fields define as following.

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## Package Outline

Dimensions are in millimeters. All dimensions are  $\pm 0.1$ mm unless otherwise specified. (Unit: mm)

